

1503.64981

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hiroshi Kondo et al.
Serial No.: 09/723,768
Conf. No.: 6836
Filed: 11/28/2000
For: APPARATUS AND METHOD
FOR PREVENTING AN
ERRONEOUS OPERATION AT
THE TIME OF DETECTION OF
A SYSTEM FAILURE
Art Unit: 2113
Examiner: Puente, Emerson C.

I hereby certify that this paper is being filed electronically with the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

13 June 06
Date

[Signature]
Registration No. 29,367
Attorney for Applicant(s)

AMENDMENT F

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action mailed April 3, 2006, please amend the application as follows: